

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

William H. ROBERTSON et al.

Serial No.: 10/600,999

Filed: June 20, 2003

**For: METHOD AND SYSTEM FOR
FACILITATING ELECTRONIC CIRCUIT
AND CHIP DESIGN USING REMOTELY
LOCATED RESOURCES**

Group Art Unit: 2825

Examiner: Whitmore, Stacy

Confirmation No.: 5787

Enter

/ SW /

02/21/2008

AFTER ALLOWANCE AMENDMENT PURSUANT TO 37 C.F.R. § 1.312

Mail Stop Issue Fee

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Applicants thank the Examiner for allowing this application. In the meantime, Applicants noticed some clerical mistakes in claims 69 and 79 and thus respectfully submit this after allowance amendment pursuant to 37 C.F.R. § 1.312 as follows.

Amendments to the Claims begin on page 2.

Remarks begin on page 12.